

The block diagram illustrates a data processing system 15. It includes an ELECTRICAL PULSE DETECTING MEANS 13, which is connected to an INPUT PORT 18 via a line 12. The INPUT PORT 18 is connected to a CPU 20. The CPU 20 is connected to PROGRAM MEMORY 21 and RANDOM ACCESS MEMORY 22. The OUTPUT PORT 23 is connected to a PRINT HEAD 7 via a line 24. A MOTOR DRIVER 25 is connected to the OUTPUT PORT 23 and a MOTOR 2. A line 16 is connected to the SERIAL PORT 19.

FIG. 2



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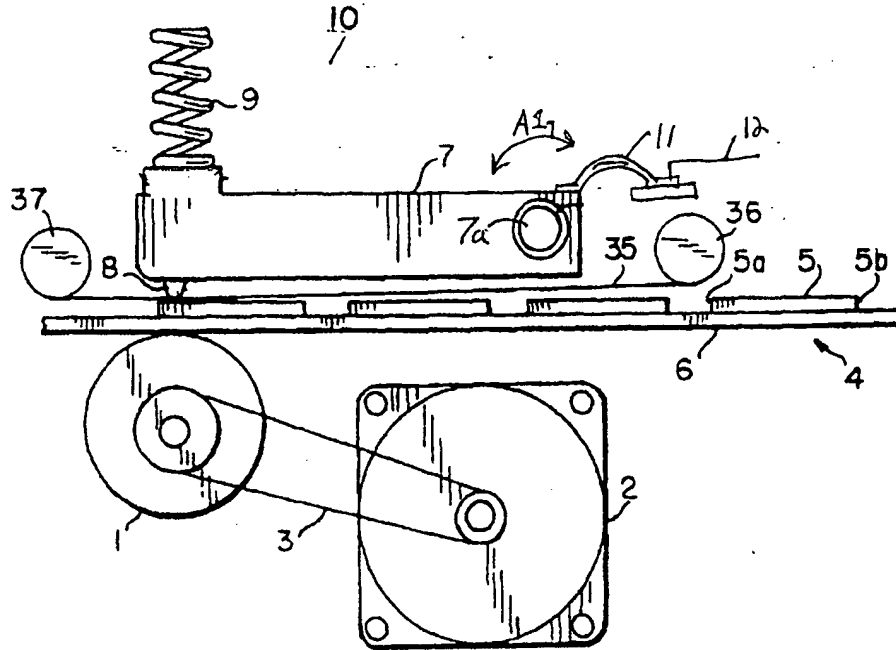


Figure 5

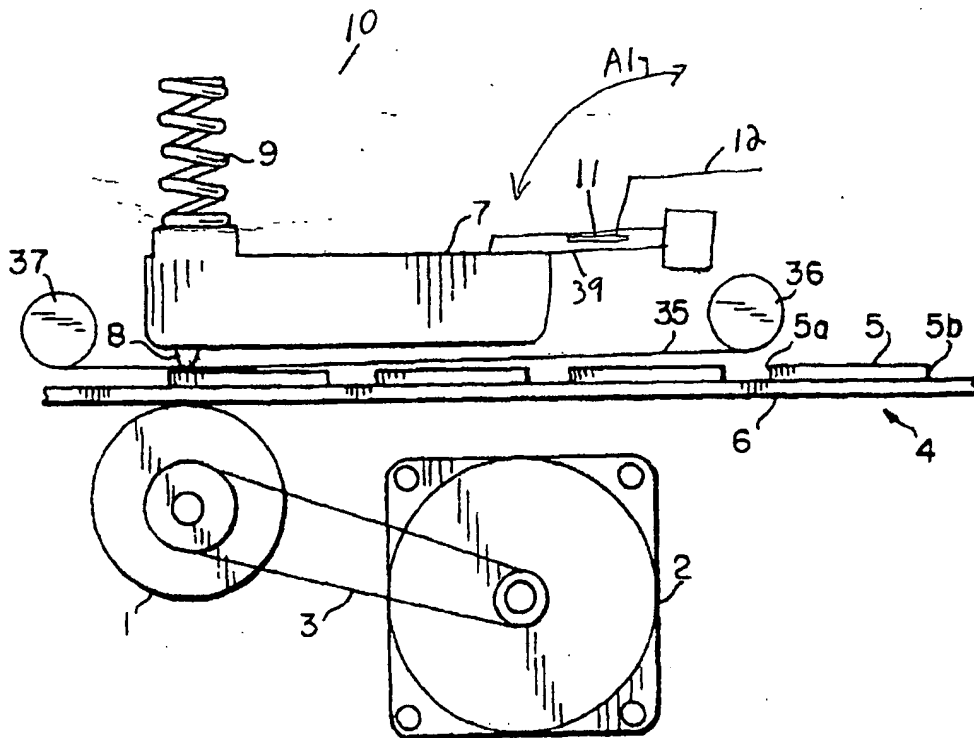


Figure 6

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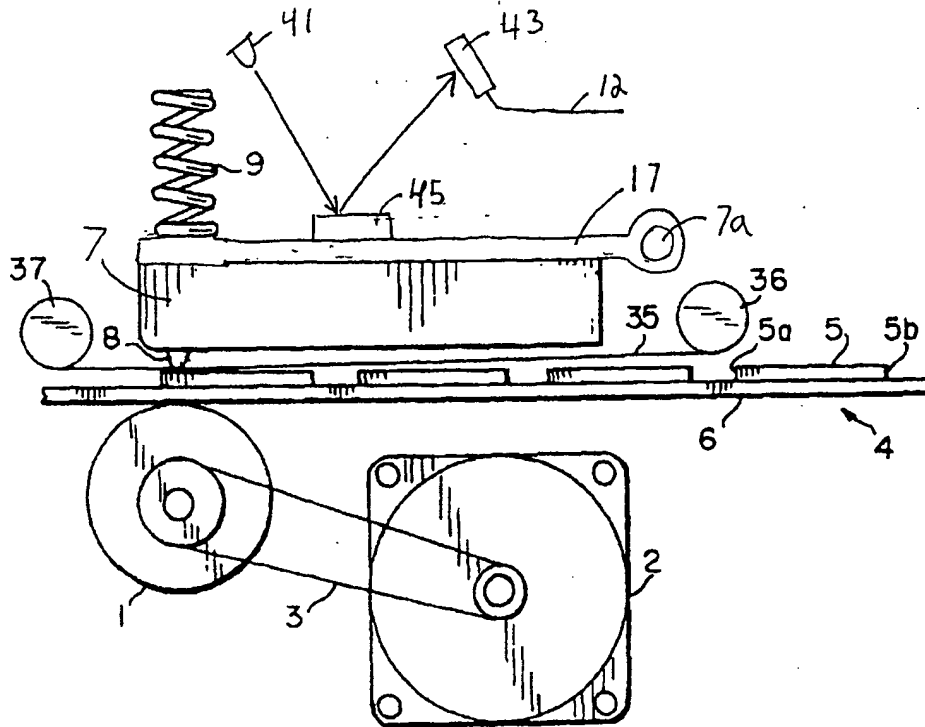


Figure 7

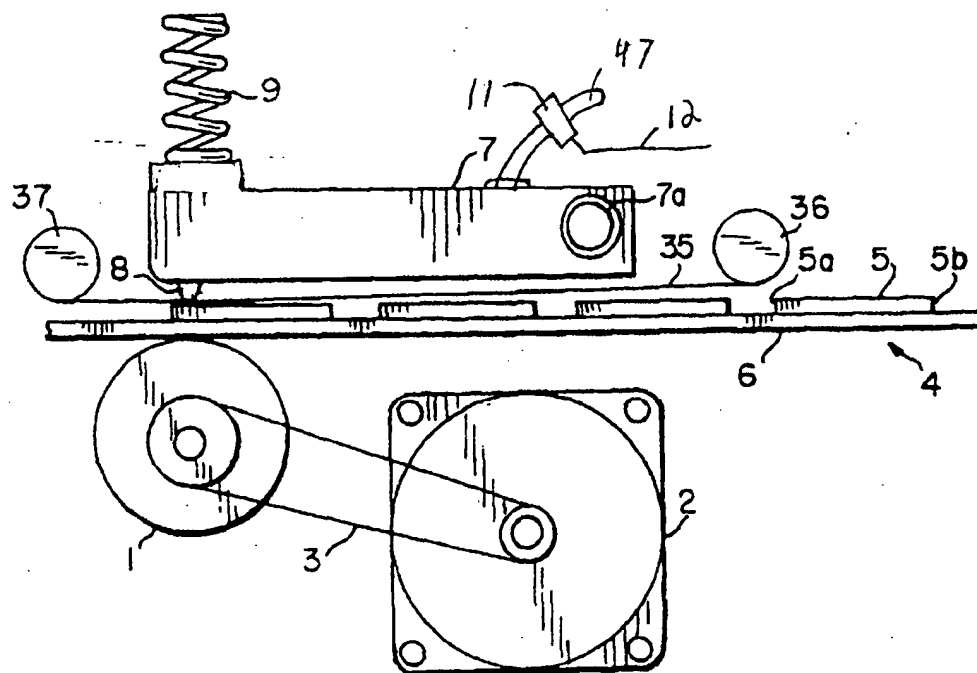


Figure 8

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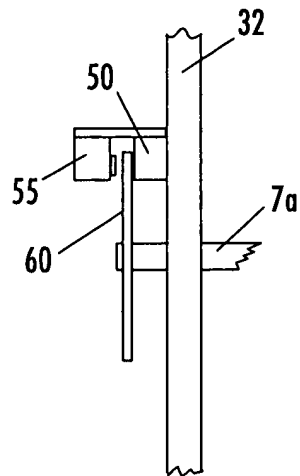


FIG. 9a

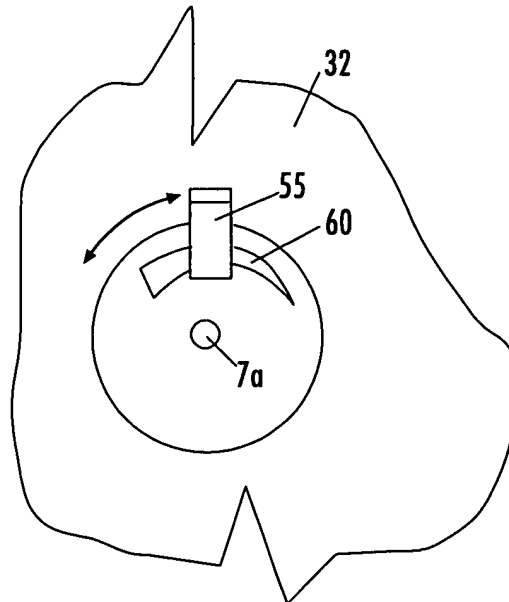


FIG. 9b

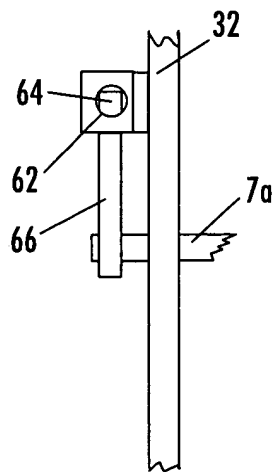


FIG. 10a

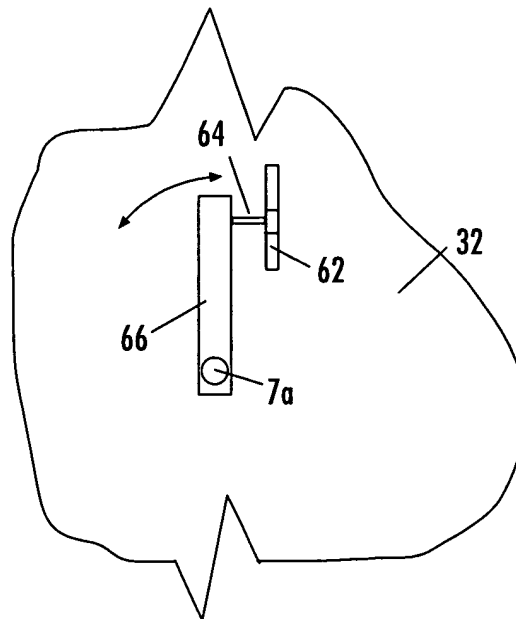


FIG. 10b